



General Chairs

Indranil Sen Gupta

*Indian Institute of Technology
Kharagpur, India*

Rolf Drechsler

University of Bremen/DFKI, Germany

Program Chair

Kamalika Datta

Hafizur Rahaman

Research in reversible computation has drawn the attention of researchers over the years. Reversible computation has a growing number of promising application areas such as low power design, testing and verification, database recovery, discrete event simulation, reversible specification formalisms, reversible programming languages, process algebras, quantum computation, etc. Initial implementations of reversible and quantum circuits have been reported recently and are seen as promising alternatives to CMOS technology.

The conference will bring together researchers, academicians and students from computer science, electronics engineering, mathematics, and physics to discuss new developments and directions for future research in reversible computation. This particularly includes applications of reversibility in quantum computation. Research papers, tutorials, tool demonstrations, and work-in-progress reports are within the scope of the conference. Invited talks by leading international experts will complete the program. A summer school will be conducted along with the conference as a parallel track specifically targetted to the beginners in this area.

Contributions, not necessarily restricted to the following topics, in Reversible Computation are invited from prospective authors:

- Architectures
- Algorithms
- Circuit Design
- Debugging
- Fault Tolerance and Error Correction
- Hardware
- Information Theory
- Physical Realizations
- Programming Languages
- Quantum Computation
- Software
- Synthesis
- Theoretical Results
- Testing
- Verification

Interested researchers are invited to submit full research papers (16 pages maximum), tutorials (16 pages maximum), as well as work-in-progress or tool demonstration papers (6 pages maximum). All accepted papers will be published at the conference in an official publication venue (probably Springer LNCS).

The 9th Conference on Reversible Computation will take place on July 6th and 7th, 2017, in Kolkata, India and will be jointly organized by: (a) Indian Institute of Technology, Kharagpur, (b) Indian Institute of Engineering Science and Technology (IIST), (c) University of Calcutta, and (d) Indian Statistical Institute Calcutta. Previous editions of this event took place as workshop in York (2009), Bremen (2010), Gent (2011), Copenhagen (2012) and was upgraded to a conference subsequently and held in Victoria (2013), Kyoto (2014), Grenoble (2015), and Bologna (2016).

Important Dates (tentative):

- Abstract Submission: Tue January 31st, 2017
- Submission Deadline: Tue February 7th, 2017
- Notification to Authors: Mon March 20th, 2017
- Final Version Due: Mon April 10th, 2017
- Conference: Thu-Fri, July 6th and 7th, 2017

Further information about the conference:

<http://www.reversible-computation.org>
info@reversible-computation.org

1st Announcement

